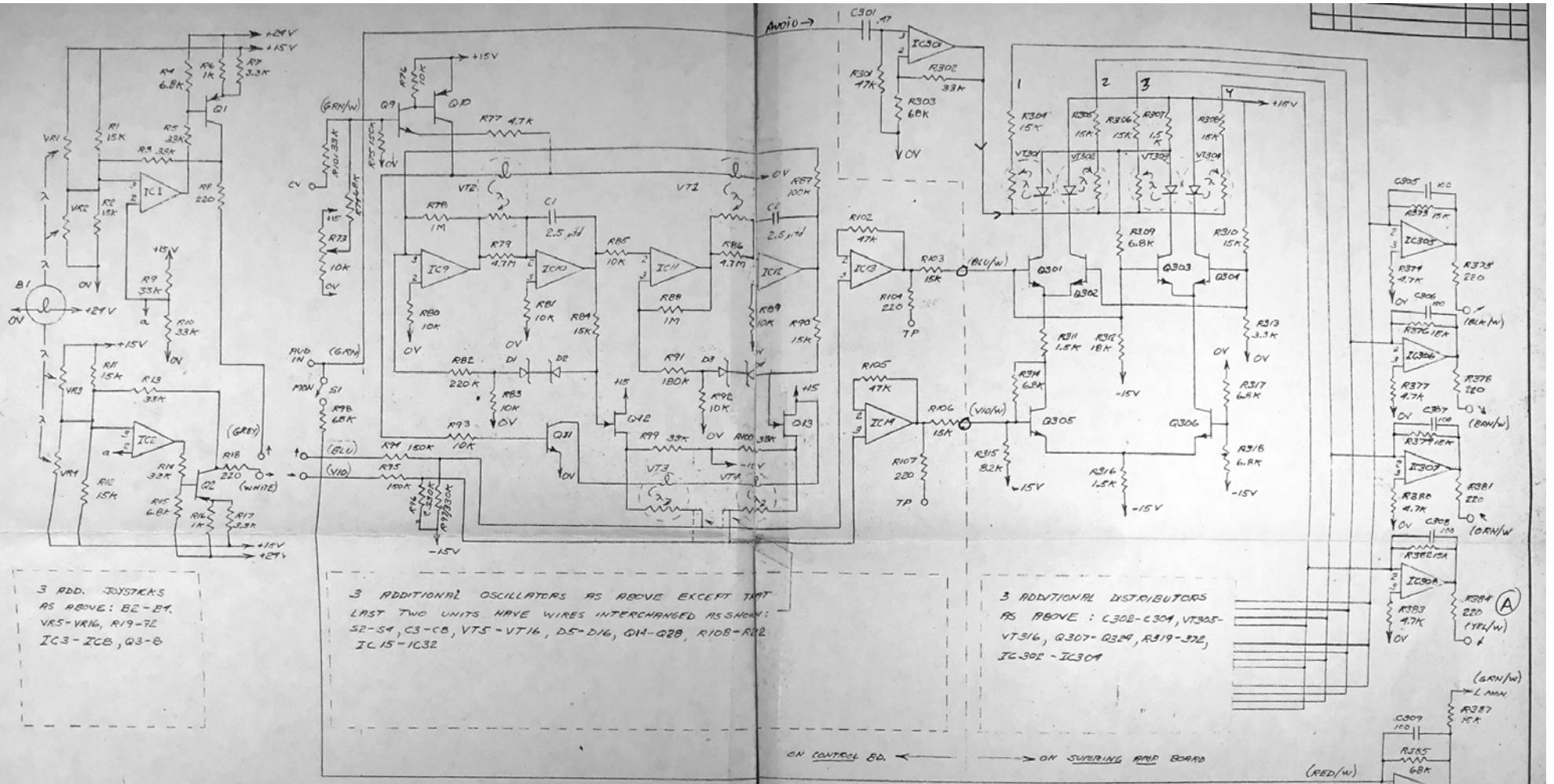
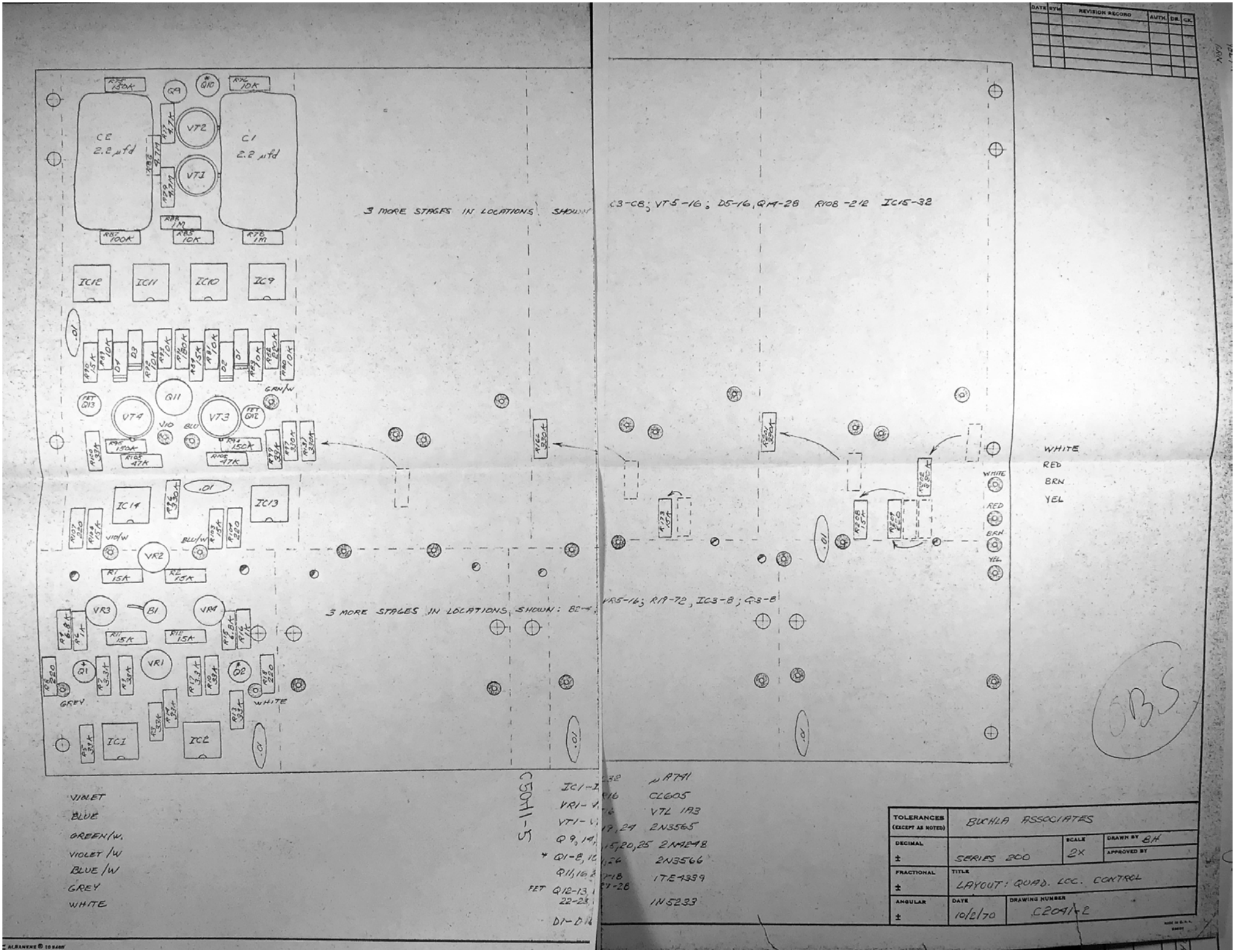


IC1 - IC32	µA741	
VR1 - VR16	CLAREX	CL605
VT1 - VT16	VACTECH	VTL 1A
Q9, 14, 19, 24	2N3565	
Q1-8, 10, 15, 20, 25	2N4248	
Q11, 16, 21, 26	2N3564	
Q12, 13, 17, 18, 22, 23, 27, 28	TIE4339	
D1 - D16	IN5233	

IC301 - IC309	µA741/L
VT301 - VT316	VACTEC VTLEC3
Q301 - Q324	2N3566

TOLERANCES (EXCEPT AS NOTED)	BUCHLA ASSOCIATES		
DECIMAL ±	SERIES 200	SCALE —	DRAWN BY BH / APPROVED BY
FRACTIONAL ±	TITLE: SCHEMATIC: MOD 204 QUAD LOC.		
ANGULAR ±	DATE 9/9/70	DRAWING NUMBER C2040	

C2040


REVISION RECORD
DATE
BY
AUTH.
DR.
CK.
C1
2.2 µfd
C2
2.2 µfd
VT1
VT2
VT3
VT4
Q9
Q10
Q11
Q12
Q13
IC1
IC2
IC9
IC10
IC11
IC12
IC13
IC14
VR1
VR2
VR3
VR4
3 MORE STAGES IN LOCATIONS SHOWN
C3-C8; VT5-16; D5-16, QM-28 R108-212 IC15-32
VR5-16; R19-72, IC3-8; Q3-8
3 MORE STAGES IN LOCATIONS SHOWN: BE-
WHITE
RED
BRN
YEL
VIOLET
BLUE
GREEN/W.
VIOLET/W
BLUE/W
GREY
WHITE
IC1-I
VR1-V
VT1-V
Q9, 14,
Q1-8, 10
Q11, 16
FET Q12-13,
22-23,
D1-D16
µA741
CL605
VTL 1A3
2N3565
2N4248
2N3566
ITE4339
IN5233
TOLERANCES (EXCEPT AS NOTED)
DECIMAL ±
FRACTIONAL ±
ANGULAR ±
BUCHLA ASSOCIATES
SERIES 200
SCALE 2X
DRAWN BY BH
APPROVED BY
TITLE
LAYOUT: QUAD. LOC. CONTROL
DATE 10/2/70
DRAWING NUMBER C2091-2