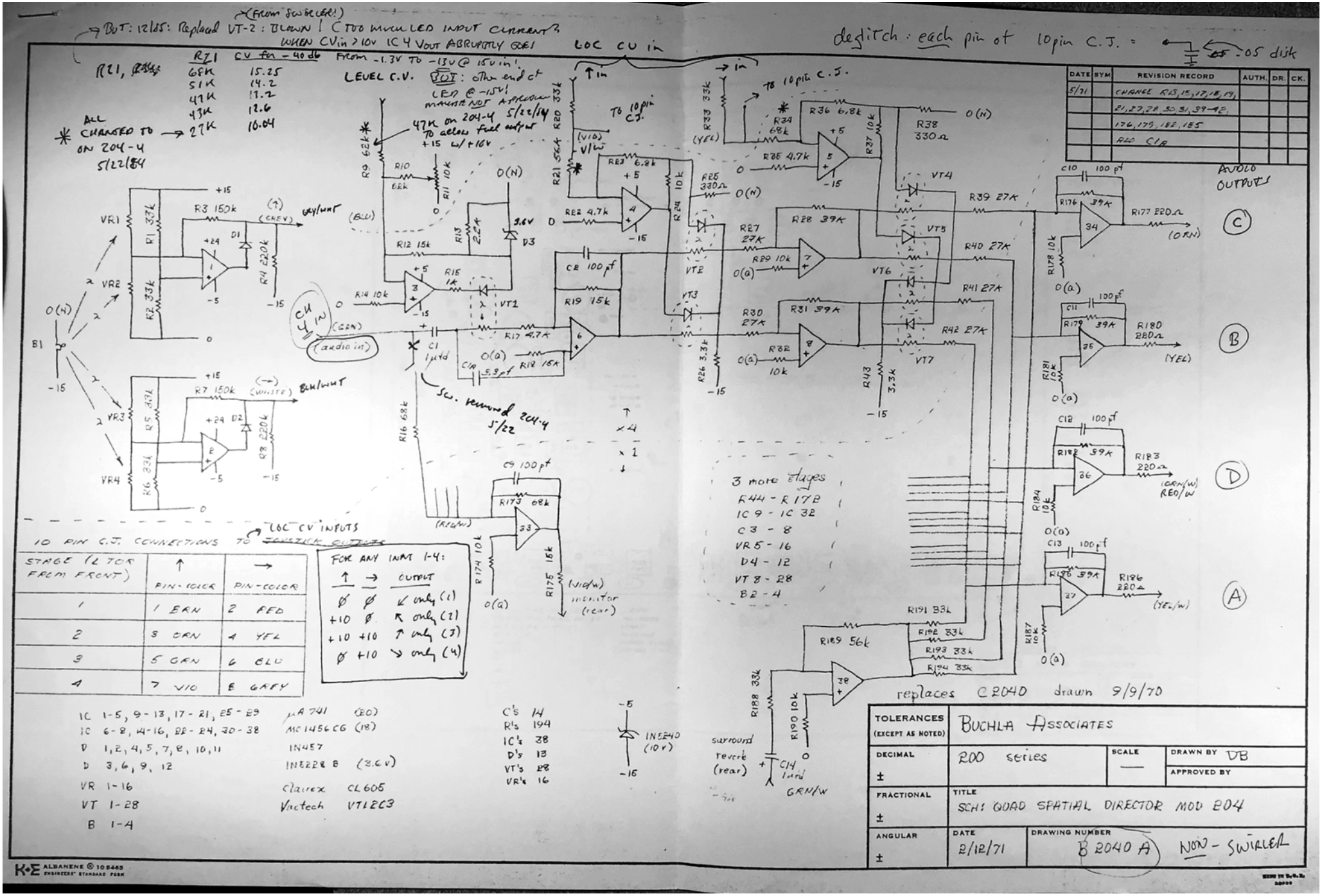


10 PIN C.J. CONNECTIONS TO LOC CV INPUTS

STAGE (L TO R FROM FRONT)	↑ PIN-COLOR	→ PIN-COLOR
1	1 BRN	2 RED
2	3 ORN	4 YEL
3	5 GRN	6 BLU
4	7 VIO	8 GRAY

FOR ANY INPUT 1-4: ↑	→	OUTPUT
Ø	Ø	↙ only (1)
+10	Ø	↖ only (2)
+10	+10	↗ only (3)
Ø	+10	↘ only (4)

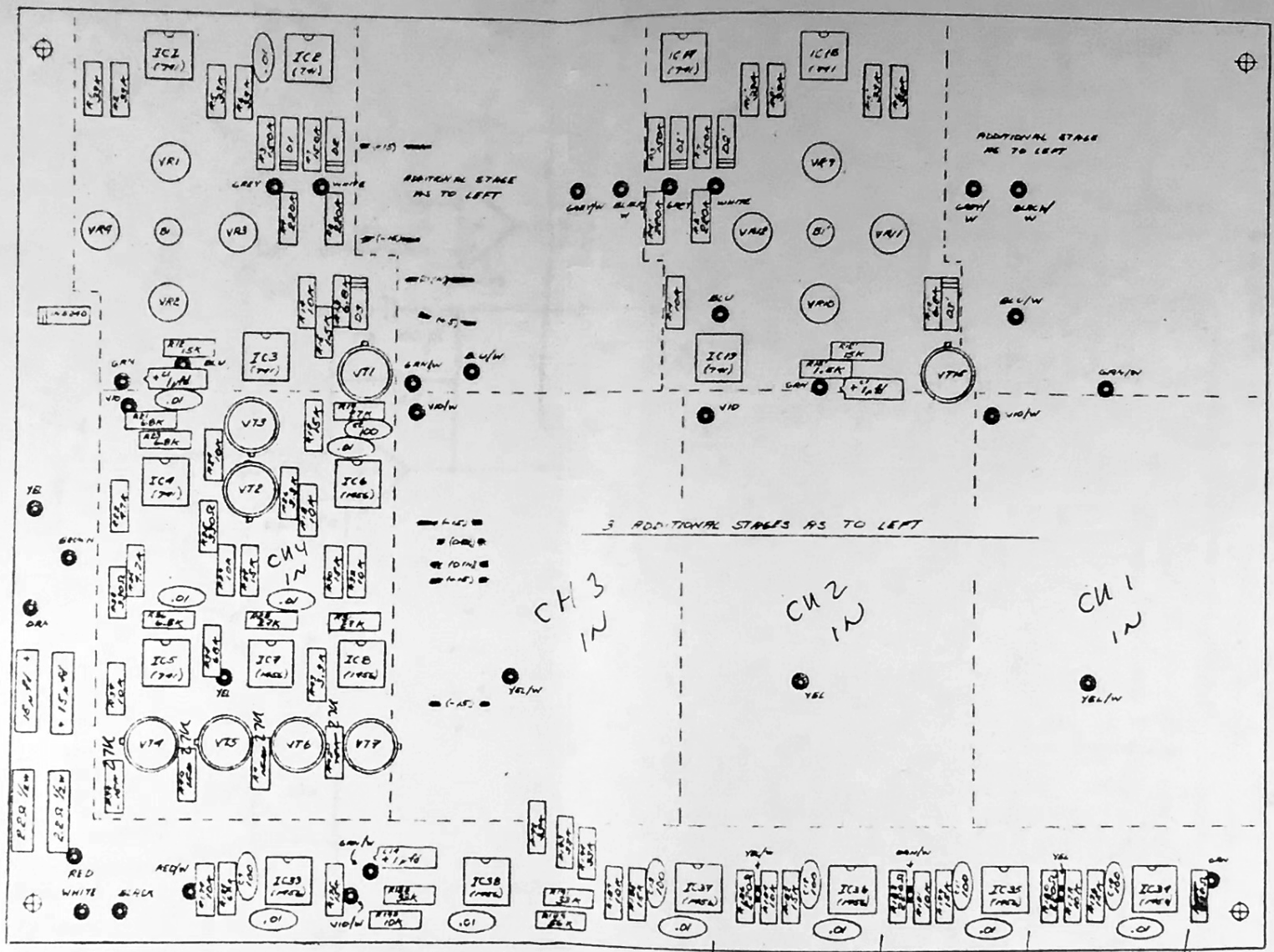
IC	1-5, 9-13, 17-21, 25-29	μA 741	(20)
IC	6-8, 14-16, 22-24, 30-32	MC1456CG	(18)
D	1, 2, 4, 5, 7, 8, 10, 11	IN457	
D	3, 6, 9, 12	IN5228B	(3.6V)
VR	1-16	Clairex	CL605
VT	1-28	Vactech	VT12C3
B	1-4		

C's	14
R's	194
IC's	38
D's	13
VT's	28
VR's	16

DATE	SYM	REVISION RECORD	AUTH.	DR.	CK.
5/71		CHANGE R13, 15, 17, 18, 19, 21, 27, 28, 30, 31, 39-42, 176, 179, 182, 185 ADD C1A			

TOLERANCES (EXCEPT AS NOTED)	BUCHLA ASSOCIATES		
DECIMAL ±	200 series	SCALE —	DRAWN BY DB
FRACTIONAL ±	TITLE SCH: QUAD SPATIAL DIRECTOR MOD 204		APPROVED BY
ANGULAR ±	DATE 2/12/71	DRAWING NUMBER B 2040 A	NON-SWITCHED

K+E ALBANENE © 10 8485 ENGINEERS' STANDARD PAPER



REPEATED SECTIONS

GREY, GREY/W
WHITE, BLACK/W

GRN, GRN/W
BLU, BLU/W
VIO, VIO/W
YEL, YEL/W

POWER LINES AT FAR LEFT

YEL
RED
ORANGE
BLACK
BROWN
WHITE

BOTTOM WIRES

RED/W
GRN/W
VIO/W
YEL/W
ORN/W
YEL
ORN

IC 1-5, 9-13, 17-21, 25-29
IC 6-8, 14-16, 22-24, 30-32
Q1, 2, 4, 5, 7, 8, 10, 11
D3, 6, 9, 12
VR1-16
VT1-28

B1-4

MATH
MICRONETICS
INTEL
INSTRON
CLAREY CL-605
VECTOR VTL3C3

REPLACES B2041-1 AND C2041-2

TOLERANCES UNLESS OTHERWISE NOTED	BUCHLA ASSOCIATES		
DECIMAL ±	SERIES 200	SCALE EX	DRAWN BY BN
FRACTIONAL ±	TITLE LAYOUT: MOD 204 QUAD LOC.		APPROVED BY
ANGULAR ±	DATE 2/19/74	DRAWING NUMBER C2041A	

4 1074