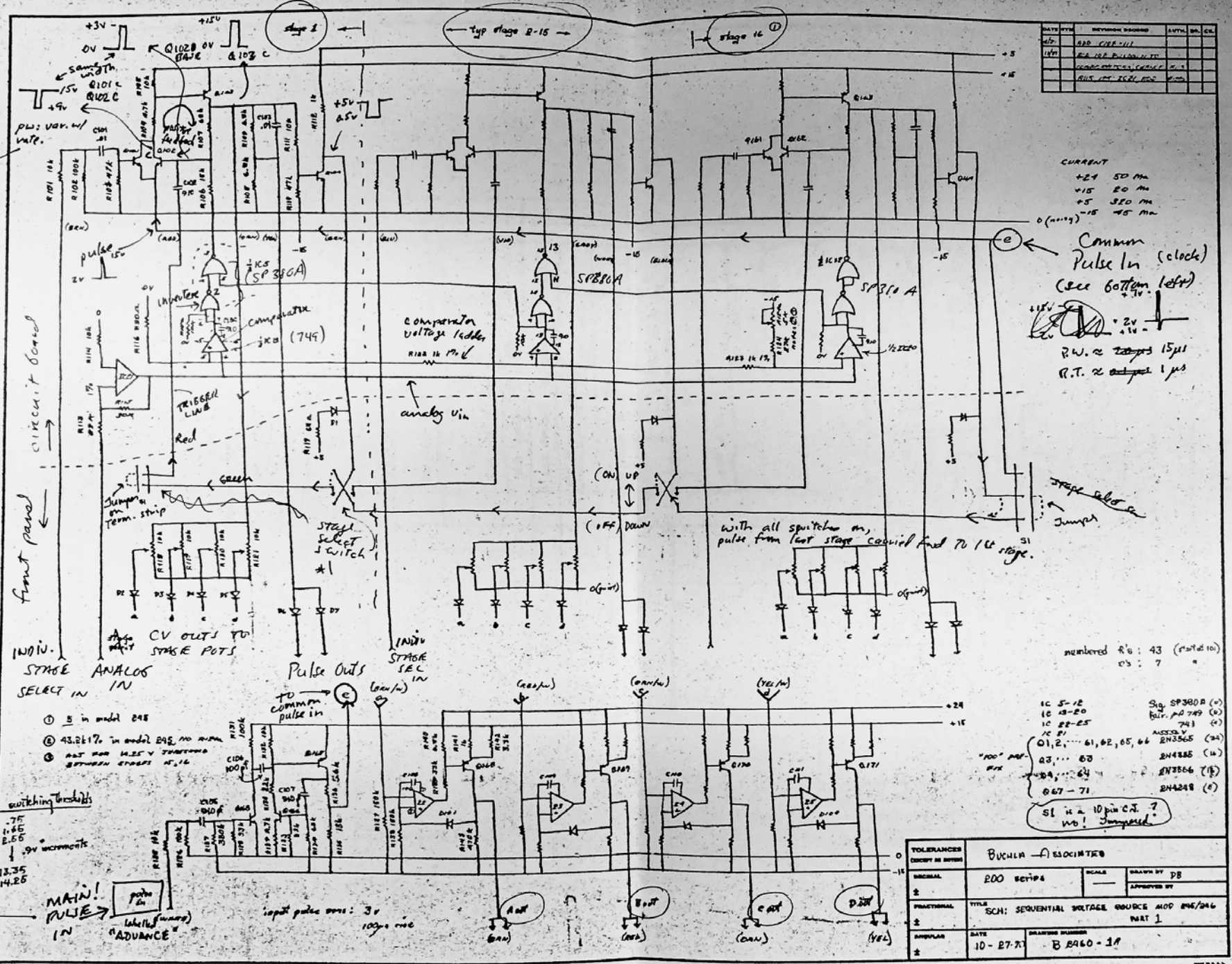


Q101, Q102
 1 pulse turns stage on, next turns it off! - actually ~~turns~~ stage turns off!
 OR gate stays low while stage active

Q101, Q102 not really a diff any in sense that one base is inv, the other not, &'s and C's joined: essentially either base going high drives both C's low and Q103 latches and inverts this condition.

14V
 7.5V
 0V
 rise time 2 μs



DATE	REV	REVISIONS	DESIGNED	AUTH	CHK
10/27/71	1	ADD CIRCUIT			
10/27/71	2	REVISED CIRCUIT			
10/27/71	3	REVISED CIRCUIT			

CURRENT
 +24 50 mA
 +15 20 mA
 +5 300 mA
 0 (-15) 45 mA

Common Pulse In (clock)
 (see bottom left)
 2V
 1V
 P.W. ≈ 20 μs 15 μs
 R.T. ≈ 1 μs

- ① 5 in model 245
- ② 43.2kΩ in model 245, NO MARK
- ③ NOT FOR USE V. THRESHOLD BETWEEN STAGES 15, 16

switching thresholds

7.5
1.65
2.55
0.9 increments
13.35
14.25

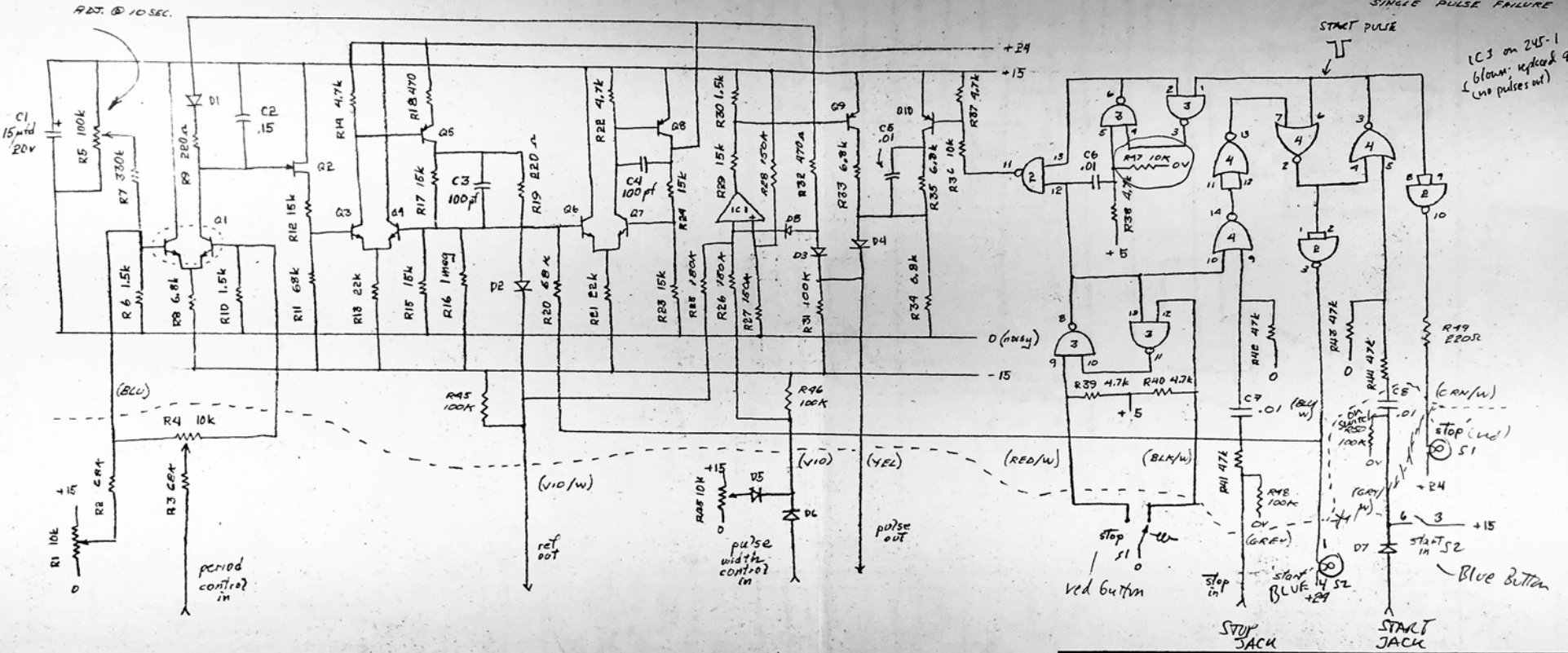
- numbered R's: 43 (partial)
 C's: 7
- 1C 5-12 Sig. SP380A (2)
 - 1C 13-20 Bol. 749 (2)
 - 1C 22-25 743 (2)
 - 1C 27 ACCESS (2)
 - 1C 28 2N3565 (24)
 - 1C 29 2N4385 (4)
 - 1C 30 2N3566 (1)
 - 1C 31 2N4248 (2)
- SI is a 10 pin C.T. 7
 No! Jumped

TOLERANCES UNLESS NOTED			
RESISTOR	±	SCALE	DRAWN BY DB
CAPACITOR	±	APPROVED BY	
MECHANICAL	±	TITLE	SCH: SEQUENTIAL VOLTAGE SOURCE MOD 245/246
ANGULAR	±	DATE	10-27-71
		DRAWING NUMBER	B 5460-1A
		PART	1

DATE	BY	REVISION RECORD	AUTH.	DR.	CK.
4/71		CHANGE R2, R3, R20, R25			
		R7, R1 / RELOCATE R28 /			
		ADD C7, R8, D8, R45-50			

NOTE: FLIPPING KEYBOARD DOES NOT HAVE R47; POSSIBLE SINGLE PULSE FAILURE

IC3 on 245-1
6 (down; replaced 9/23/85
(no pulses out)



- c's 5 + bypasses
R's 45
- D1-B IN457
 - Q1 2N4045
 - Q2 ITE 4339
 - Q3,4,6,7 2N3565
 - Q5,8-10 2N4248
 - IC 1 SN72741 (T.T.)
 - IC 2 MC 858 P (Not)
 - IC 3 MC 846 P → MC SN7400
 - IC 4 SP 380 A (sig)

TOLERANCES (EXCEPT AS NOTED)		BUHLER ASSOCIATES	
DECIMAL	200 series	SCALE	DRAWN BY DB
±		—	APPROVED BY
FRACTIONAL	TITLE		
±	SCH: SEQUENTIAL VOLTAGE SOURCE MOD 245, 246 (PART 2: PULSER)		
ANGULAR	DATE	DRAWING NUMBER	
±	10-29-70	B 2460-2A	